DSD-2/3

DISSIPPOS DISCOVER NOTES

PARTS LIST

CASING

2201018200 Case

2218053800 Pedal

2218054800 Pedal

2221047200 Panel 2202011601

2350305000 Base

75228520

2235030400 Pedal Mat 2247038600 Knob

Cover

Effect Board

First Edition

SPECIFICATIONS

Power Source 9V Battery x 1 or AC adaptor (BOSS PSA Series)

Current Draw 45mA or 60mA @9V Sampling Time 200ms (MIN) to 800ms (MAX)

Delay Time 50ms (MIN) to 800ms (MAX) Frequency Response Sampling / Delay : 40Hz to 7kHz (+1 / -3 dB) Direct: 10Hz to 60kHz (+1 / -3 dB)

Residual Noise Sampling / Delay mode : -95dBm (IHF-A)

Normal mode: -100dBm or less (IHF-A)

Input Impedance $1 M\Omega$ (FET input)

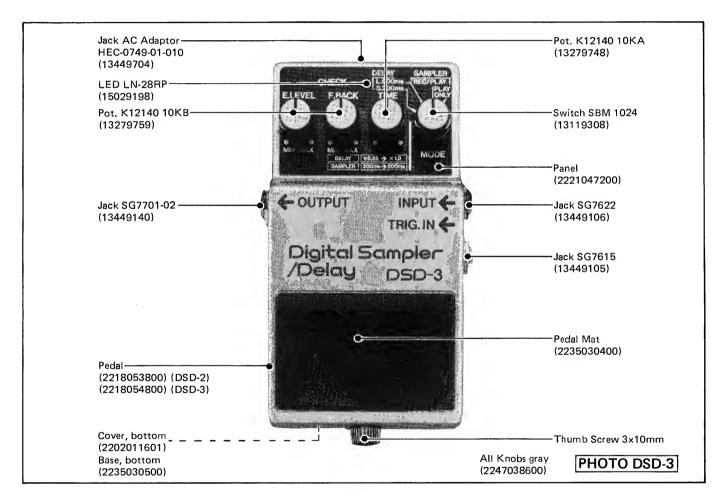
Output Load Impedance 10K Ω or more

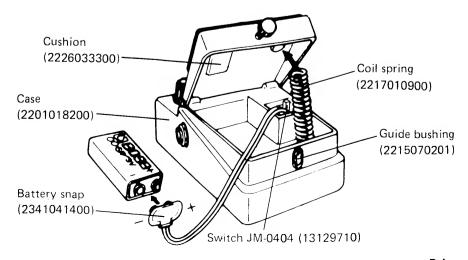
Weight 450g / 1lbs

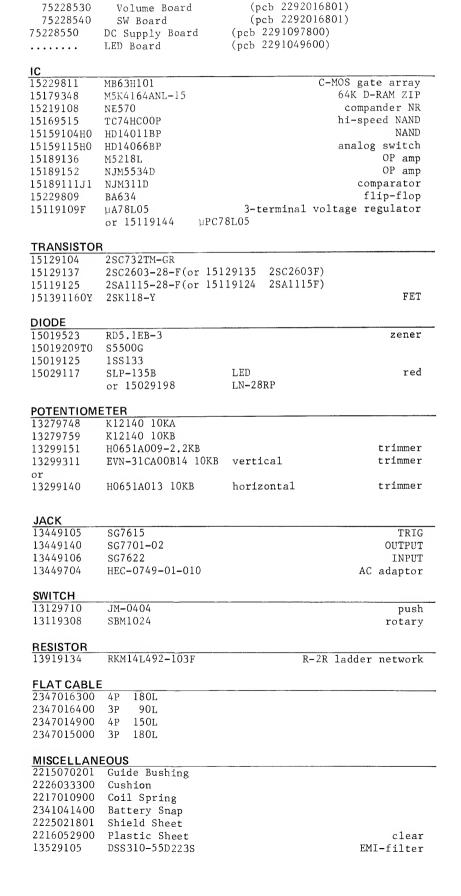
*This notes includes the contents of the DSD-2 First Edition and makes it obsolate.

*The difference between DSD-2 and DSD-3 is nothing but the pedal.

*DSD-2のサービスノート第一版は廃版とし本サービスノートに併合します。 *DSD-2とDSD-3の違いはペダルだけで他は全く同じです。







(DSD-2)

(DSD-3)

Replacement Effect Board includes Volume Board and SW Board.

(pcb 2292016801)

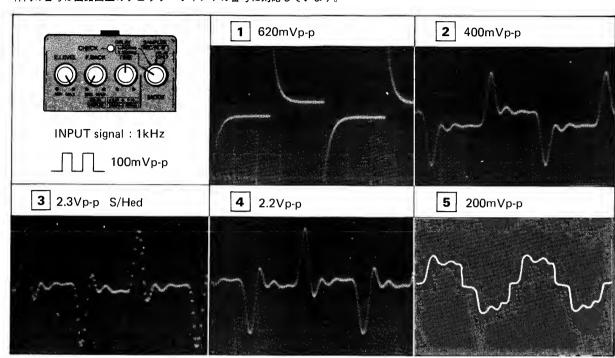
bottom

bottom

gray

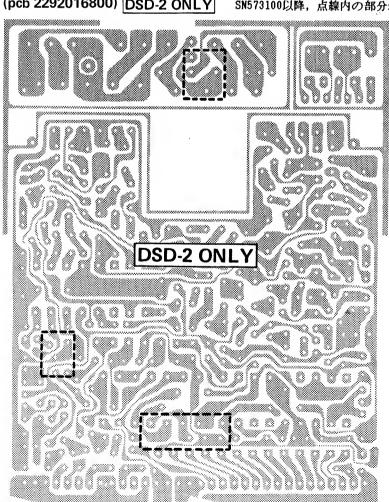
WAVEFORMS

Boxed numbers correspond to those (check point) on the schematic diagram. 枠内の番号は回路図上のチェック・ポイントの番号に対応しています。

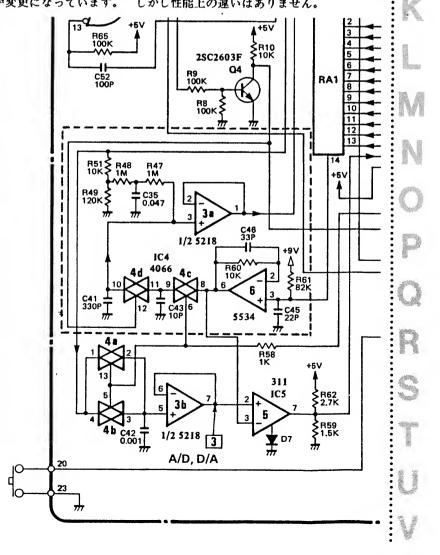


Prior to SN573100 SN573100未満 (pcb 2292016800) DSD-2 ONLY

The circuit is changed due to factory requirements -no performance difference between this and modified one.
SN573100以降,点線内の部分が変更になっています。 しかし性能上の違いはありません。



View from foil side



EFFECT BOARD

75228520 (pcb 2292016801)

SN573100-UP · · · (DSD-2)

Replacement PCB is supplied in a set of three PCBs as shown below. 補修用基板としては下記に示す三種の基板を含む一枚基板です。

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17

SW BOARD

75228540 (pcb 2292016801)

VOLUME BOARD →

75228530 (pcb 2292016801)

EFFECT BOARD -

75228520 (pcb 2292016801)

DC SUPPLY BOARD

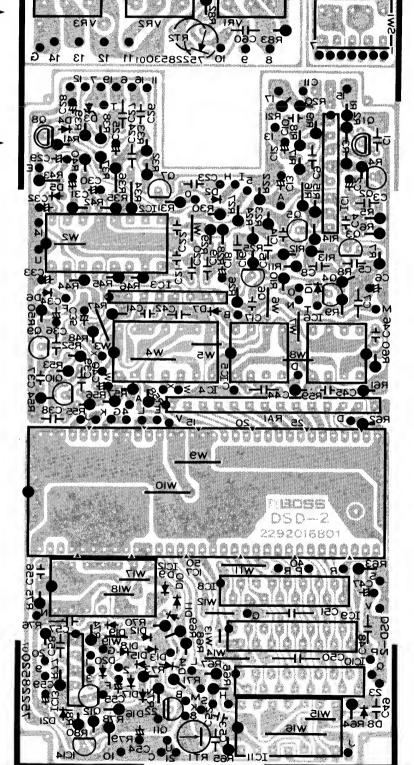
75228550 (pcb 2291097800)



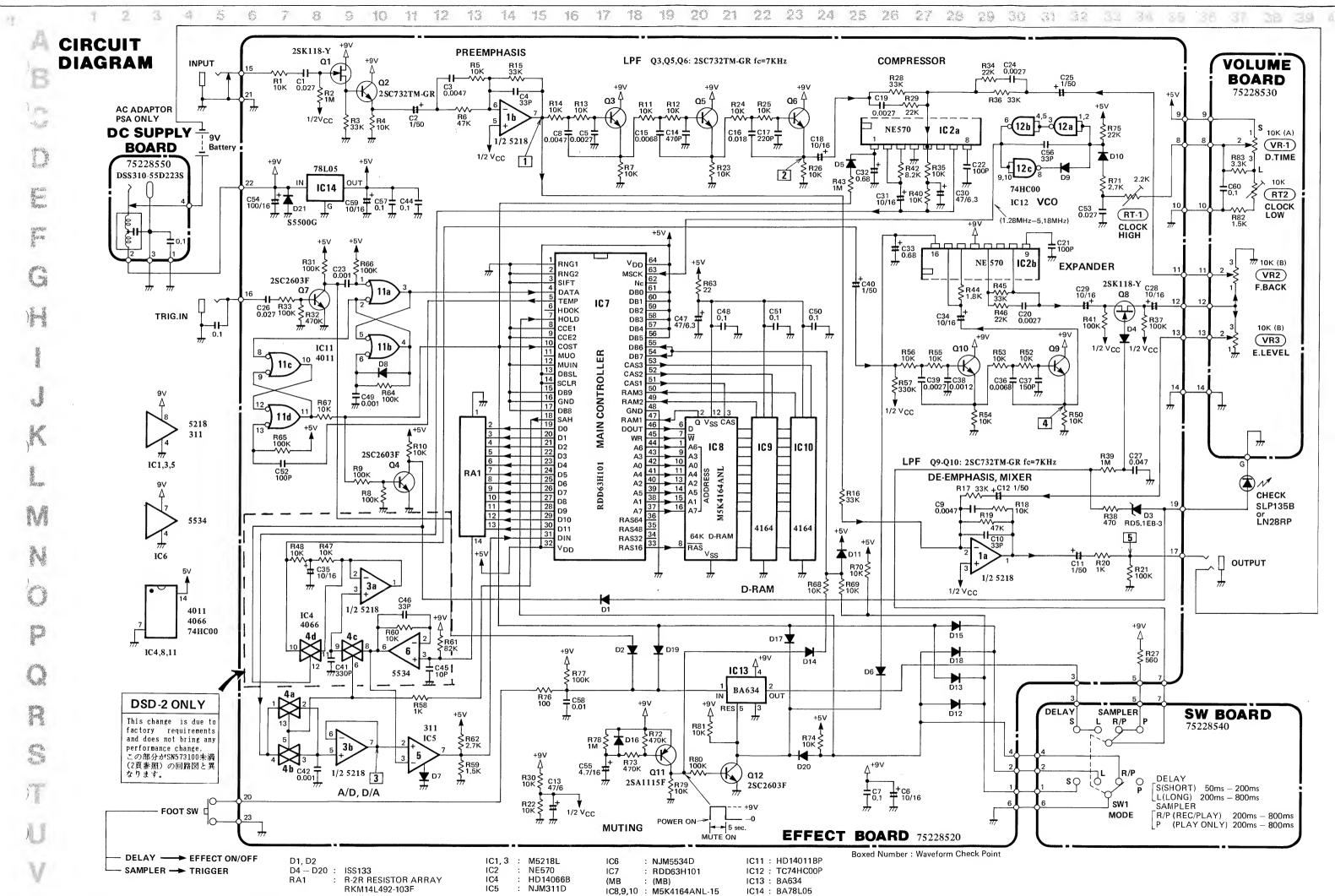
LED BOARD

(pcb 2291049600)

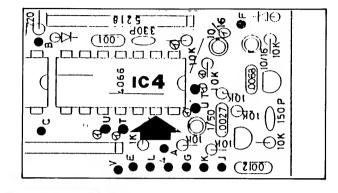




View from foil side



This adjustment is to set the range of Master Clock (MSCK) frequency at IC12 VCO.



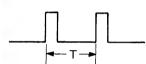
1. 上限

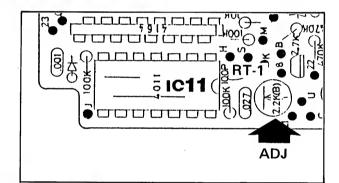
Connect the scope to pin 13 of IC4 (or IC7 pin 18 SAH).

オシロスコープをIC 4のピン13かIC7のピン18に接続する。









Adjust RT-1 on Effect Board for T = 12.12μ s (82.5K \pm 1kHz).

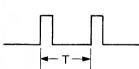
エフェクト基板上のRT-1を調整してT=12.12μs(82.5k±1kHz) にオス

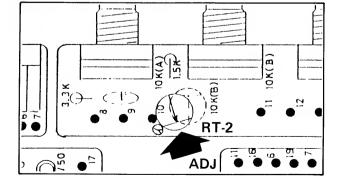
The MSCK should be 5.28MHz ± 64kHz.

この時マスタ・クロックは5.28MHz±64kHzになる。

2. Low End 2. 下限







Adjust RT-2 on Volume Board for T = 50μ s ($20K \pm 0.2$ kHz)

ボリューム基板上のRT-2を調整してT=50 $\mu_{\rm S}$ (20k±0.2kHz)にする。

The MSCK should be 1.28M ± 12.8kHz.

この時マスタ・クロックは1.28MHz±12.8kHzになる。

DSD-2/3

SEP. 1986

IC DATA

